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26021 7590 02/10/2004

HOGAN & HARTSON L.L.P.
 500 S. GRAND AVENUE
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 LOS ANGELES, CA 90071-2611

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Eirogzezh VAKI Zodeh (Depositor's name)
hirodezh VAKI Zodeh (Signature)
APRIL 28, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/511,620	02/23/2000	Tomomi Momohara	005702-20068	3356

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A PLURALITY OF WELLS, TEST METHOD OF TESTING THE SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, AND TEST DEVICE WHICH EXECUTES THE TEST METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	05/10/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
FENTY, JESSE A	2815	257-499000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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(A) NAME OF ASSIGNEE

Kobushiki Kaisha Toshiba

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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☒ Advance Order - # of Copies 10

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Laura J. McBlair 4/28/04

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